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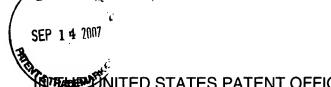
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TRANSMITTAL		Application / Cont. 1	10.	10//91,924 / 3818	
		Filing Date		March 02, 2004	
FORM		First Named Invent	tor	Brian T. Brunn	
(to be used for all correspondence after initial filing)		Examiner Name		Siu M. Lee	
Mail Stop: AMENDMENT		Art Unit		2611	
Express Mail Receipt No.		Patent No.			
Total Number of Pages in This Submission		Attorney Docket Nu	umber	X-1549 US	
ENCLOSURES (check all that apply)					
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X-1549 US 10/791,924



ED STATES PATENT OFFICE

PATENT

Conf. No.: 3818

Applicant: Brian T. Brunn et al.

Assignee: Xilinx, Inc.

Title: Bit-Edge Zero Forcing Equalizer

Serial No.: 10/791,924 File Date: March 2, 2004

Siu M. Lee 2611 Examiner: Art Unit:

Docket No.: X-1549 Conf. No.: 3818

Mail Stop Amendment COMMISSIONER FOR PATENTS P.O. Box 1450

Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the First Office Action mailed from the Patent Office on June 12, 2007, please replace/substitute the following claim(s) as indicated.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.

Amended Replacement Drawing Sheets are included as attachments to this paper.